

IN THE CLAIMS:

Please amend the claims as follows.

Claim 1 (Original): An organic electroluminescent device, comprising:

first and second substrates facing and spaced apart from each other, the first and second substrates having a display region and a peripheral region, the display region including a plurality of pixel regions and a dummy pixel region;

driving thin film transistors respectively adjacent to each of the plurality of pixel regions on the inner surface of the first substrate;

first connection electrodes respectively connected to the driving thin film transistors;

a first electrode on an entire inner surface of the second substrate;

a sidewall on the first electrode at a boundary of each of the plurality of pixel regions and the dummy pixel region;

an organic electroluminescent layer on the first electrode;

second electrodes on the organic electroluminescent layer so that a second electrode is present in each of the plurality of pixel regions and the dummy pixel region, respectively, the second electrodes in each of the plurality of pixel regions are respectively connected to the first connection electrodes; and

a sealant attaching the first and second substrates.

Claim 2 (Original): The device according to claim 1, further comprising:
a pad in the peripheral region on an inner surface of the first substrate; and
a second connection electrode connected to the pad, the second connection electrode formed in the same layer and formed of the same material as the first connection electrodes, wherein the first electrode is connected to the second connection electrode.

Claim 3 (Original): The device according to claim 1, wherein the second electrode in the dummy pixel region is electrically floating.

Claim 4 (Original): The device according to claim 1, wherein each of the driving thin film transistors includes a driving active layer, a driving gate electrode, and driving source and driving drain electrodes.

Claim 5 (Original): The device according to claim 2, wherein each of the driving thin film transistors includes a driving active layer, a driving gate electrode, and driving source and driving drain electrodes, and wherein the pad includes the same material as the driving source and driving drain electrodes.

Claim 6 (Original): The device according to claim 4, further comprising switching thin film transistors connected to the driving thin film transistors, wherein each of the switching thin film transistors includes a switching active layer, a switching gate electrode, and switching source and switching drain electrodes.

Claim 7 (Original): The device according to claim 6, wherein the driving active layer and the switching active layer include polycrystalline silicon.

Claim 8 (Previously Presented): The device according to claim 6, wherein the switching source electrode is connected to a data line, wherein the switching drain electrode is connected to the driving gate electrode, wherein the switching gate electrode is connected to the gate line.

Claim 9 (Original): The device according to claim 1, further comprising a power line connected to the driving thin film transistors.

Claim 10 (Original): The device according to claim 1, further comprising storage capacitors connected to the driving thin film transistors.

Claim 11 (Original): The device according to claim 1, wherein the first electrode is an anode injecting holes into the organic electroluminescent layer, and wherein the second electrodes are cathodes injecting electrons into the organic electroluminescent layer.

Claim 12 (Original): The device according to claim 11, wherein the first electrode includes one of indium-tin-oxide (ITO) and indium-zinc-oxide (IZO).

Claim 13 (Original): The device according to claim 11, wherein the second electrodes include one of calcium (Ca), aluminum (Al) and magnesium (Mg).

Claim 14 (Original): The device according to claim 2, further comprising an auxiliary electrode between the first electrode and the second connection electrode, wherein the auxiliary electrode includes the same material as the second electrodes.

Claim 15 (Original): The device according to claim 1, wherein the dummy pixel region surrounds the plurality of pixel regions.

Claim 16 (Original): The device according to claim 1, further comprising an auxiliary insulating layer between the first electrode and at least one of the sidewalls.

Claims 17 - 34 (Canceled).

Claim 35 (New): The device according to claim 1, wherein a gap is defined between the first and second substrates with the first connection electrodes spanning the gap to electrically interconnect the driving thin film transistors on the first substrate with respective ones of the second electrodes on the second substrate.